

AMENDMENTS TO THE CLAIMS

Please amend the claims as follows:

1. (CURRENTLY AMENDED) An image signal processor for performing image processing on a first image signal representative of an image of a subject field captured by an imaging device to produce a plurality of second image signals, comprising:

a first memory for storing the first image signal;

a plurality of image processors for each performing image processing on the stored first image signal to produce the plurality of second image signals, wherein all second image signals are different from each other; and

a second memory for storing each of the plurality of second image signals produced,

wherein said plurality of image processors include types and parameters of the image processing such that at least one of the types and parameters of the image processing are different between said plurality of image processors, and

wherein said second memory is a non-volatile memory.

2. (PREVIOUSLY PRESENTED) The image signal processor according to claim 1, wherein the types of image processing performed by each of said plurality of image processors include at least one selected from a group consisting of a change of brightness, a change of gradation change characteristics, a correction of a color temperature, a change of saturation, a change of a contour, a change of a compression ratio and a change of a black level of the first image signal stored in said first memory,

the parameters of image processing being of the at least one selected from the group,

said plurality of image processors performing the image processing of the at least one selected from the group on the first image signal stored in said first memory according to the parameters of image processing.

3. (ORIGINAL) The image signal processor according to claim 1

wherein each of said plurality of image processors corresponds to one of a plurality of display units which are provided for visualizing the images represented by the plurality of second image signals stored in said second memory,

each of said plurality of image processors processing, according to the parameters, the plurality of second image signals to be displayed on one of the plurality of display units which corresponds to said image processor.

4. (ORIGINAL) The image signal processor according to claim 3 wherein the plurality of display units include a CRT (Cathode Ray Tube) display and an image printer.

5. (ORIGINAL) The image signal processor according to claim 2, further comprising a divider circuit for dividing the first image signal stored in said first memory into a highlight area and a shadow area,

each of said plurality of image processors performing the image processing in which at least one of the types and the parameters of the image processing differs between the highlight area and the shadow area.

6. (ORIGINAL) The image signal processor according to claim 1 wherein said second memory is detachably connected to said image signal processor.

7. (ORIGINAL) An image signal processor for performing image processing on a first image signal representative of an image of a subject field captured by an imaging device to produce a second image signal, comprising:

a first memory for storing therein the first image signal;

a plurality of image processors for each performing image processing, different from each other, on the stored first image signal to produce a third image signal different from each other;

a second memory for storing therein the third image signals produced, and

an image composer circuit for composing the third image signals to produce the second image signal,

wherein said plurality of image processors include types and parameters of the image processing such that at least one of the types and parameters of the image processing are different between said plurality of image processors.

8. (ORIGINAL) The image signal processor according to claim 7, further comprising a divider circuit for dividing the first image signal stored in said first memory into a highlight area and a shadow area,

wherein said plurality of image processors are provided correspondingly to the highlight area and the shadow area, each of said plurality of image processors performing the image processing for one of the highlight area and the shadow area to produce the third image signals.

9. (CURRENTLY AMENDED) A method of processing a first image signal representative of an image of a subject field captured by an imaging device to produce a plurality of second image signals, comprising the steps of:

storing the first image signal in a first memory;

performing image processing on the stored first image signal according to parameters of image processing different from each other to produce the plurality of second image signals; and

storing each of the produced plurality of second image signals in a second memory,

wherein the second memory is a non-volatile memory.

10. (ORIGINAL) Imaging apparatus comprising:
an imaging device for capturing an image of a subject field and producing a first image signal representative of the subject field;
a first memory for storing therein the first image signal;

a plurality of image processors for each performing image processing, different from each other, on the stored first image signal to produce the second image signal different from each other;

a second memory for storing therein the second image signals produced; and

an image composer circuit for composing the second image signals to produce a third image signal,

said plurality of image processors including types and parameters of the image processing such that at least one of the types and parameters of the image processing are different between said plurality of image processors,

the types of image processing including at least one selected from a group consisting of a change of brightness, a change of gradation change characteristics, a correction of a color temperature, a change of saturation, a change of a contour, a change of a compression ratio and a change of a black level of the first image signal stored in said first memory,

the parameters of image processing being of the at least one selected from the group,

whereby said plurality of image processors perform the image processing of the at least one selected from the group on the first image signal stored in said first memory according to the parameters of image processing.

11. (PREVIOUSLY PRESENTED) The image signal processor according to claim 1, wherein each of said plurality of image processors directly receives the stored first image signal as input.

12. (PREVIOUSLY PRESENTED) The method of processing according to claim 9, wherein each of said plurality of image processing directly receives the stored first image signal as input.

13. (PREVIOUSLY PRESENTED) The image signal processor according to claim 5, wherein a brightness of the highlight area is decreased or a brightness of the shadow area is increased or both.

14. (CURRENTLY AMENDED) An image processing method, comprising:
retrieving a first image data;
generating a plurality of second image data based on the first image data; and
storing each of the plurality of second image data into a memory,
wherein a combination of imaging parameters and values applied to generate each second image data is unique for each second image data among the plurality of second image data, and
wherein the memory is a non-volatile memory.

15. (PREVIOUSLY PRESENTED) The image processing method of claim 14, wherein in the step of generating the plurality of second image data includes directly processing the first image when generating each of the plurality of second image data.

16. (PREVIOUSLY PRESENTED) The image processing method of claim 14, wherein the imaging parameters include at least one of gain, gradation control, luminance-chrominance, edge enhancement, saturation emphasis, and compression ratio.

17. (PREVIOUSLY PRESENTED) The image processing method of claim 16, wherein the step of generating the plurality of second image data includes particularizing at least one second image data for a display type.

18. (PREVIOUSLY PRESENTED) The image processing method of claim 17, wherein the display type is one of a cathode ray tube (CRT) or a printer.

19. (PREVIOUSLY PRESENTED) The image processing method of claim 14, wherein the step of generating the plurality of second image data includes:

determining whether a brightness of the first image data is above a predetermined maximum or below a predetermined minimum; and

performing a black level correction on the first image data if the brightness of the first image data is determined to be above the predetermined maximum or below the predetermined minimum.

20. (PREVIOUSLY PRESENTED) The image processing method of claim 19, wherein the step of determining whether the brightness of the first image data is above the predetermined maximum or below the predetermined minimum includes:

determining that the brightness of the first image data is below the predetermined minimum if the number of black level pixels of the first image data is greater than a first predetermined number; and

determining that the brightness of the first image data is above the predetermined maximum if the number of white level pixels of the first image data is greater than a second predetermined number.

21. (PREVIOUSLY PRESENTED) The image processing method of claim 14, wherein the step of generating the plurality of second image data based on the first image data includes:

dividing the first image data into a plurality of areas, each area differing in brightness;

adjusting the brightness of the plurality of areas by a corresponding plurality of adjustment amounts; and

combining the adjusted plurality of areas.

22. (PREVIOUSLY PRESENTED) The image processing method of claim 21, wherein

the step of dividing the first image includes dividing the first image area into a highlight area and a shadow area, and

the step of adjusting the brightness includes reducing the brightness of the highlight area and increasing the brightness of the shadow area.

23. (CURRENTLY AMENDED) The image processing method of claim 22, wherein the step of adjusting the brightness includes:

reducing an exposure value of the highlight area by a first fixed value; and
increasing an exposure value of the shadow area by a second fixed value; or
both.

24. (NEW) The image signal processor according to claim 3, further comprising:

a data compressor configured to compress the plurality of second image signals prior to being stored in the second memory,

wherein a compression ratio for compressing each of the plurality of second image signals is based on a type of the display unit designated for the second image.

25. (NEW) The image signal processor according to claim 7, further comprising:

a data compressor configured to compress the second image signal,

wherein a compression ratio for compressing the second image signal is based on a type of the display unit designated for the second image signal.

26. (NEW) The image processing method according to claim 9, further comprising:

determining a compression ratio for each of the plurality of second image signals based on a type of the display unit designated for the second image signal, and

compressing each of the plurality of second image signals prior storing in the second memory with the determined compression ratio.

27. (NEW) The imaging apparatus according to claim 10, further comprising:
a data compressor configured to compress the third image signal,
wherein a compression ratio for compressing the third image signal is based on a type of the display unit designated for the third image signal.

28. (NEW) The image processing method according to claim 14, further comprising:

determining a compression ratio for each of the plurality of second image data based on a type of the display unit designated for the second image data, and

compressing each of the plurality of second image data prior storing in the second memory with the determined compression ratio.

29. (NEW) The image signal processor according to claim 7, wherein the second memory is non-volatile.

30. (NEW) The image processing method according to claim 8, wherein the second memory is non-volatile.

31. (NEW) The imaging apparatus according to claim 10, wherein the second memory is non-volatile.

32. (NEW) The image signal processor according to claim 24, wherein the compression ratio for a CRT display is higher than the compression ratio for a printer.